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1 Fast online pointer analysis

Martin Hirzel, Daniel Von Dincklage, Amer Diwan, Michael Hind

April 2007 **Transactions on Programming Languages and Systems**

Publisher: ACM Request Permissions

Full text available: Pdf (430.96 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 120, Downl

Pointer analysis benefits many useful clients, such as compiler optimiza common programming language features such as dynamic loading, refle pointer analysis difficult. ...

Keywords: Pointer analysis, class loading, native interface, reflection

2 Reducing the harmful effects of last-level cache polluters with an OS

Livio Soares, David Tam, Michael Stumm

November 2008 **MICRO '08:** Proceedings of the 2008 41st IEEE/ACM Interr

Publisher: IEEE Computer Society

Full text available: Pdf (681.23 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 152, Downlo

It is well recognized that LRU cache-line replacement can be ineffective localized memory access patterns. Specifically, in last-level processor ca inserting non-reuseable ...

3 LLVM: A Compilation Framework for Lifelong Program Analysis & Tr

Chris Lattner, Vikram Adve

March 2004 **CGO '04:** Proceedings of the international symposium on Coc directed and runtime optimization

Publisher: IEEE Computer Society

Full text available: Pdf (256.90 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 44, Downloads (12 Months): 286, Downl

This paper describes LLVM (Low Level Virtual Machine),a compiler frame lifelongprogram analysis and transformation for arbitrary programs,by p compilertransformations at compile-time, ...

4 Dynamic Class Hierarchy Mutation

Lixin Su, Mikko H. Lipasti
March 2006 **CGO '06:** Proceedings of the International Symposium on Co
Publisher: IEEE Computer Society
Full text available: Pdf (377.30 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 19, Downloa

Class hierarchies in object-oriented programs are used to capture varioi represent, allowing programmers to encapsulate common attributes in t in lower-level ...

5 A multi-version cache replacement and prefetching policy for hybrid
André Seifert, Marc H. Scholl
August 2002 **VLDB '02:** Proceedings of the 28th international conference
Publisher: VLDB Endowment
Full text available: Pdf (202.49 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 21, Downloa

This paper introduces MICP, a novel multiversion integrated cache repla efficient cache and transaction management in hybrid data delivery net and sporadically ...

6 Optimal multistream sequential prefetching in a shared cache
Binny S. Gill, Luis Angel D. Bathen
October 2007 **Transactions on Storage (TOS)** , Volume 3 Issue 3
Publisher: ACM Request Permissions
Full text available: Pdf (1.04 MB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 22, Downloads (12 Months): 110, Downl

Prefetching is a widely used technique in modern data storage systems. prefetching algorithms known as *sequential prefetching*. There are two sequential prefetching ...

Keywords: Adaptive prefetching, asynchronous prefetching, cache poll multistream read, optimal prefetching, prefetch wastage, prestaging, se trigger distance

7 New results on web caching with request reordering
Susanne Albers
June 2004 **SPAA '04:** Proceedings of the sixteenth annual ACM symposi architectures
Publisher: ACM Request Permissions
Full text available: Pdf (186.52 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 19, Downloa

We study web caching with request reordering. The goal is to maintain requests can be served at low cost. To improve cache hit rates, a limite [6], who recently ...

Keywords: approximation, batch, cache, competitive, document, offlin

8 LLVA: A Low-level Virtual Instruction Set Architecture
Vikram Adve, Chris Lattner, Michael Brukman, Anand Shukla, Brian Gaeke
December 2003 **MICRO 36:** Proceedings of the 36th annual IEEE/ACM Inte
Publisher: IEEE Computer Society
Full text available: Pdf (196.08 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 92, Downloa

A virtual instruction set architecture (V-ISA) implementedvia a processc great flexibility to processor designers. Recentexamples such as Crusoe hardware instruction ...

9 Mostly static program partitioning of binary executables
 Efe Yardimci, Michael Franz
June 2009 **Transactions on Programming Languages and Systems**
Publisher: ACM Request Permissions
Full text available: Pdf (2.23 MB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 32, Downloads (12 Months): 263, Downl

We have built a runtime compilation system that takes unmodified sequ on off-the-shelf multiprocessors using dynamic vectorization and loop-le Azure, is purely software ...

Keywords: Continuous compilation and optimization, binary translatior

10 Phase-based adaptive recompilation in a JVM
Dayong Gu, Clark Verbrugge
April 2008 **CGO '08:** Proceedings of the sixth annual IEEE/ACM internati optimization
Publisher: ACM
Full text available: Pdf (431.13 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 63, Downloa

Modern JIT compilers often employ multi-level recompilation strategies also the most highly optimized, balancing optimization costs and expect code to compile and ...

Keywords: adaptive optimization, hardware counters, java, runtime te

11 System synthesis of synchronous multimedia applications
Gang Qu, Miodrag Potkonjak
February 2003 **Transactions on Embedded Computing Systems (TECS)**
Publisher: ACM Request Permissions
Full text available: Pdf (286.29 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 76, Downlo

Modern system design is being increasingly driven by applications such

communications, which have intrinsic quality of service (QoS) requirem resolution. One of the most ...

Keywords: high-level embedded systems synthesis, on-chip memory n

12 The quest for scalable support of data-intensive workloads in distribu
Ioan Raicu, Ian T. Foster, Yong Zhao, Philip Little, Christopher M. Moretti, A
June 2009 **HPDC '09:** Proceedings of the 18th ACM international sympo computing
Publisher: ACM Request Permissions
Full text available: Pdf (1.28 MB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 18, Downloads (12 Months): 217, Downl

Data-intensive applications involving the analysis of large datasets ofter resources, for which data locality can be crucial to high throughput and approach that ...

Keywords: data diffusion, data management, data-aware scheduling, f

13 Java object header elimination for reduced memory consumption in
Kris Venstermans, Lieven Eeckhout, Koen De Bosschere
September 2007 **Transactions on Architecture and Code Optimization**
Publisher: ACM Request Permissions
Full text available: Pdf (722.38 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 78, Downloa

Memory performance is an important design issue for contemporary cor processor/memory speed gap. This paper proposes a space-efficient Jav consumption of 64-bit Java virtual machines. ...

Keywords: 64-bit implementation, Java object model, Virtual machine,

14 Caching XML Web Services for Mobility
May 2003 **Queue** , Volume 1 Issue 3
Publisher: ACM Request Permissions
Full text available: Html (35.15 KB), Pdf (311.20 KB) Additional Information: full citat

Bibliometrics: Downloads (6 Weeks): 149, Downloads (12 Months): 1229, Dov

15 Simulation-based verification using Temporally Attributed Boolean L
S. K. Panda, Arnab Roy, P. P. Chakrabarti, Rajeev Kumar
September 2008 **Transactions on Design Automation of Electronic Sys**
Publisher: ACM Request Permissions
Full text available: Pdf (1.49 MB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 63, Downloa

We propose a specification logic called Temporally Attributed Boolean (T which allows us to: (i) represent assertions succinctly, (ii) incorporate d

design intentions. ...

Keywords: Bus verification, instruction semantics verification, interrup simulation based verification, temporal logic, timing verification

16 A two-phase escape analysis for parallel java programs
Kyungwoo Lee, Samuel P. Midkiff
September 2006 **PACT '06:** Proceedings of the 15th international conferenc techniques
Publisher: ACM Request Permissions
Full text available: Pdf (288.22 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 40, Downloa

Thread escape analysis conservatively determines which objects may be escape analysis is useful for a variety of purposes—finding races in mult synchronization, allocating ...

Keywords: compilation models, consistency models, escape analysis, j

17 HMTT: a platform independent full-system memory trace monitoring
Yungang Bao, Mingyu Chen, Yuan Ruan, Li Liu, Jianping Fan, Qingbo Yuan,
June 2008 **SIGMETRICS '08:** Proceedings of the 2008 ACM SIGMETRIC modeling of computer systems
Publisher: ACM Request Permissions
Full text available: Pdf (984.65 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 14, Downloads (12 Months): 128, Downl

Memory trace analysis is an important technology for architecture resea optimization, and application performance improvements. Many approac such as simulation, binary ...

Keywords: DIMM, HMTT, memory trace, real system

18 Online expansion of rare queries for sponsored search
Andrei Broder, Peter Ciccolo, Evgeniy Gabrilovich, Vanja Josifovski, Donald
April 2009 **WWW '09:** Proceedings of the 18th international conference
Publisher: ACM
Full text available: Pdf (785.09 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 23, Downloads (12 Months): 213, Downl

Sponsored search systems are tasked with matching queries to relevant matching algorithms expand the user's query using a variety of externa these expansion-based ...

Keywords: query expansion, sponsored search, tail queries

19 Multiprocessor performance estimation using hybrid simulation

Lei Gao, Kingshuk Karuri, Stefan Kraemer, Rainer Leupers, Gerd Ascheid, H
June 2008 **DAC '08:** Proceedings of the 45th annual Design Automation
Publisher: ACM Request Permissions
Full text available: Pdf (858.95 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 21, Downloads (12 Months): 128, Downl

With the growing number of programmable processing elements in toda designs, the synergy required for the development of the hardware arch also increasing. In ...

Keywords: HySim, MPSoC, address recovery, cache simulation, cross r estimation

20 Categorizing web search results into meaningful and stable categori
Bill Kules, Jack Kustanowitz, Ben Shneiderman
June 2006 **JCDL '06:** Proceedings of the 6th ACM/IEEE-CS joint confere
Publisher: ACM Request Permissions
Full text available: Pdf (460.86 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 19, Downloads (12 Months): 142, Downl

When search results against digital libraries and web resources have lim meaningful and stable category information can enable better overviews proposes six fast-feature techniques ...

Keywords: browsing, categorization, classification, metadata, open dir

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